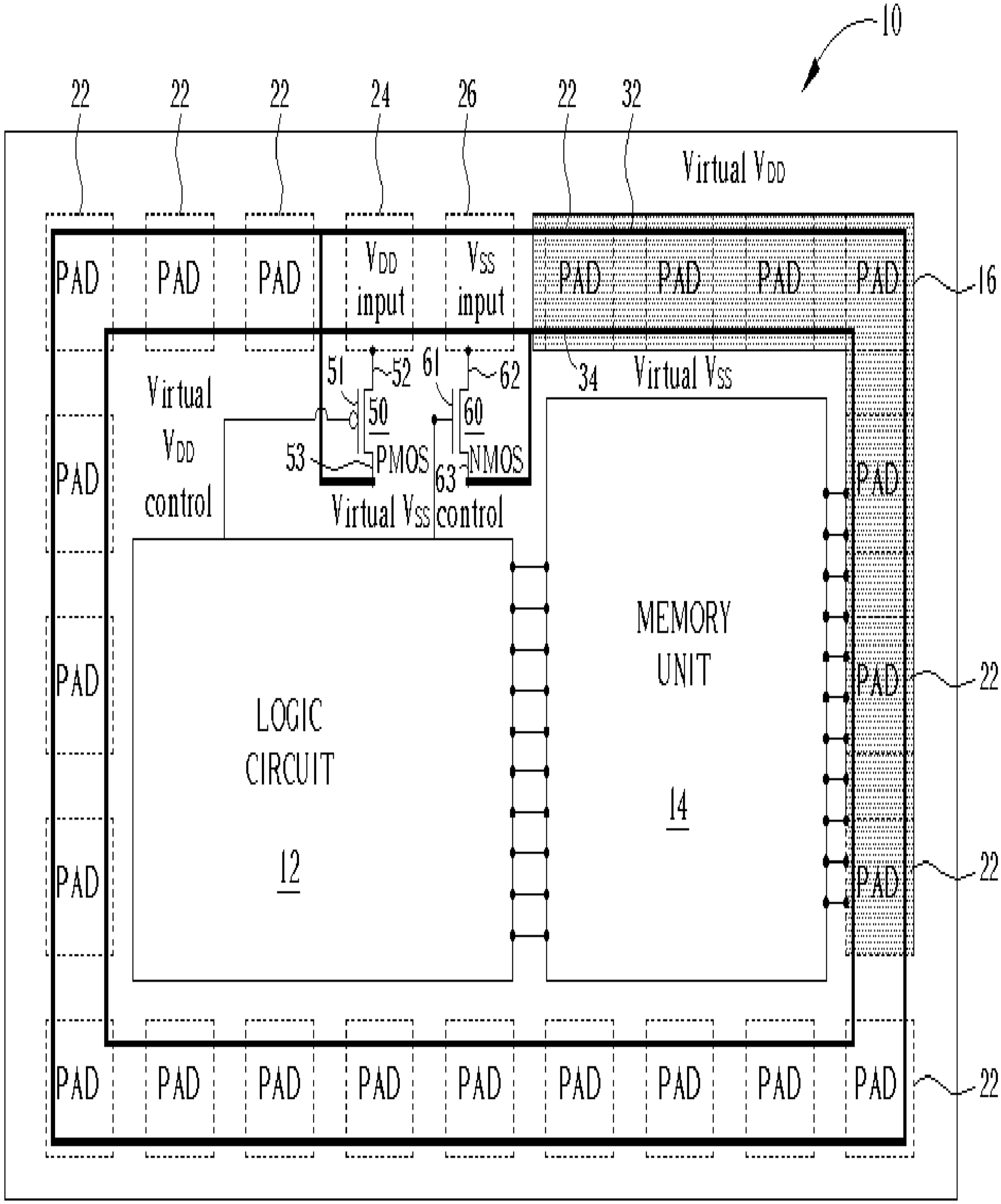
10
22
22
22
24
26
22
32
Virtual V_{DD}
PAD
PAD
PAD
V_{DD} input
V_{SS} input
PAD
PAD
PAD
PAD
16
51
52
61
62
34
Virtual V_{SS}
Virtual V_{DD} control
50
60
53
PMOS
63
NMOS
Virtual V_{SS} control
PAD
PAD
PAD
PAD
PAD
PAD
MEMORY UNIT
14
LOGIC CIRCUIT
12
22
22
PAD
PAD
PAD
PAD
PAD
PAD
PAD
PAD
PAD
22

Fig. 1